IN THE UNITED STATES PATENT & TRADEMARK OFFICE

ATTORNEY DOCKET NUMBER: 10004241-1
IN RE APPLICATION OF: U.S. Patent No. 6,914,374
USPTO CONFIRMATION CODE: 6334
APPLICATION NO.: 10/043,376
FILED: Jan. 09, 2002
EXAMINER: Joseph Williams
GROUP ART UNIT: 2879
FOR: PLANAR ELECTRON EMITTER APPARATUS WITH IMPROVED EMISSION AREA AND METHOD OF MANUFACTURE
37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION
HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS
Sir:
The following is a request for a certificate of correction in Serial Number 10/043,376, now Patent Number 6,914,374.
A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.
All errors were the fault of the USPTO, no fee required. In the event that a further fee is required, please charge the amount to Deposit Account No. 082025.
All errors were the fault of the applicant and, accordingly, please charge \$100.00 to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.
The errors were the fault of both the applicant and USPTO and, accordingly, please charge \$\frac{\frac{5100.00}}{100.00}\$ to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application are as follows:

In column 9, line 5, in Claim 1, after "surface of" insert - - a semiconductor layer above - -.

(Amendment to claims filed on Nov. 15, 2004, page 2, in Claim 1, lines 8-9)

In column 9, line 6, in Claim 1, after "wherein the" insert -- exposed surface of the --. (Amendment to claims filed on Nov. 15, 2004, page 2, in Claim 1, lines 9-10)

In column 9, line 9, in Claim 1, after "of the" insert - - exposed surface of the - -. (Amendment to claims filed on Nov. 15, 2004, page 2, in Claim 1, line 11)

In column 10, line 8, in Claim 8, after "configured" delete "to" and insert - - to have a variable thickness, to thereby - -, therefor. (Amendment to claims filed on Nov. 15, 2004, page 4, in Claim 23, lines 7-8)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

Name: Douglas M. Gilbert

Registration No.: 27,196

Attorney/Agent of Record

HEWLETT-PACKARD COMPANY Intellectual Property Administration 3404 E. Harmony Road Mail Stop 35 Fort Collins, CO 80528

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO.

: 6,914,374

APPLICATION NO.: 10/043,376

ISSUE DATE

: Jul. 05, 2005

INVENTOR(S)

: Henryk Birecki, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 9, line 5, in Claim 1, after "surface of" insert - - a semiconductor layer above - -.

In column 9, line 6, in Claim 1, after "whercin the" insert - - exposed surface of the - -.

In column 9, line 9, in Claim 1, after "of the" insert - - exposed surface of the - -.

In column 10, line 8, in Claim 8, after "configured" delete "to" and insert - - to have a variable thickness, to thereby - -, therefor.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

HEWLETT-PACKARD COMPANY Intellectual Property Administration 3404 E. Harmony Road Mail Stop 35 Fort Collins, CO 80528

This collection of information is required by 37 CFR 1.322, and 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to properties, and accounting the Conference of the Conference of the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerces, P.O. Box 1450, Alexandria, VA 22313-1450, DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patent, P.O. Box 1450, Alexandria, VA 22313-1450.